IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

K. ISHIBASHI, et al.

Application No.:

Rule 1.53(b) Continuation of U.S. Patent Application

Serial No. 09/780,393, filed February 12, 2002

Filed:

On even date herewith

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Art Unit in parent:

2824

Examiner in parent:

R. Elms

<u>UNDER 37 CFR §1.97 & 1.98</u>

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 June 25, 2003

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO-1449 which list documents cited in parent application Serial No. 09/780,393.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, Deposit Account No. 01-2135 (520.39604CX1).

Respectfully submitted,

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520.39604CX1

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Application Number

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On Even Date Herewith

First Named Inventor

Art Unit

Not Yet Assigned

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Examiner Name

Attorney Docket Number

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U.S. PATENT DOCUMENTS					
Examiner Initials'	Cite No.¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 5,465,231	11-1995	Ohsaki	
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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY		Where Relevant Passages or Relevant Figures Appear	T ⁶
		JP 7287994	10-1995			
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OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS				
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		Takai et al., "A 250Mv/s/pin 1Gb Double Data Rate SDRAM with a Bi-Directional Delay and an Inter-Bank Shared Redundancy Scheme", 1999 IEEE Int'l Solid-State Circuits Conference, pp. 418-419		
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Examiner	Date	
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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